टाटा मूलभूत अनुसंधान संस्थान हैदराबाद
TATA INSTITUTE OF FUNDAMENTAL RESEARCH HYDERABAD
प्लाट नं. ३६/पी, गोपनपल्ली ग्राम, सेरिलिंगमपल्ली मंडल, रंगा रेड्डी जिला,
हैदराबाद – ५०० १०७, तेलंगाना.
Plot No.36/P, Gopanpally Village, Serilingampally Mandal, Ranga Reddy District,
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Public Tender Ref. No. TIFR/PD/CA24-02/24-25/M/02 for Supply, Installation and Commissioning of High-Performance Computing (HPC) Cluster for TIFR Hyderabad

Sub: Clarification in Pre-Bid Meeting against Tender Ref. No. TIFR/PD/CA24-02/24-25/M/02 for Supply, Installation and Commissioning of High-Performance Computing (HPC) Cluster for TIFR Hyderabad.

Dear Bidders/Vendors,

With reference to above Public tender, the following bidders are participated in the pre-bid meeting on 22-05-2024 at 11.00 Hrs.

1. M/s. Silvertouch Technologies, Ahmedabad

2. M/s. Netweb Technologies, Hyderabad

3. M/s. Micropoint Computers, Mumbai

4. M/s. Datacipher Solutions, New Delhi

The above bidders participated through Zoom for the Technical and Commercial details of the above Tender.

Technical and Commercial clarification are as follows:

1	**Page No – 3, Point no -1**
	The bidders should have minimum three successful installations at Government/ Public/ Private Research organizations /institutions /Manufacturing industries using architecture and technologies to those being proposed in their quotation against this tender across the country within the last five years.
	In addition, the following condition should also be satisfied.
	☐ At least one installation of minimum 80% or
	☐ At least two installations of each 60% or
	☐ At least three installations of each 40% of the tender value
	Purchase order with final commissioning report with the mention of compute architecture details duly signed by customer should be Submitted with the technical bid. The contact details

	of the Customer's technical person should be provided. TIFR-H may independently contact them to verify the claim	
	Clarification Sought: Requesting to change bidders to bidders/OEM to allow small bidders as well	**TIFR Hyderabad Response:** The request will be considered for evaluation by the technical committee
2	**Page No – 5 Point - a** The OEM/bidder should carry out below listed benchmark programs on 10 TF, 30 TF and 50 TF peak performance configurations of the proposed solution and also produce the extrapolated outputs of the 100 TF, 200 TF and fully offered solution in peak performance configurations. It is not necessary to run the benchmark codes on the same quoted processor mentioned in the tender. However, the produced results should match with the results of the offered solution as a part of the acceptance test. The maximum allowable deviation from the extrapolated results in the acceptance test should be less than 3%. The results (with TFLOP and core count where applicable) should be presented in an output file and included in the technical bid. • High Performance Linpack (HPL) Benchmark should be provided for the above mentioned configurations and the same will be considered for technical evaluation. The minimum required performance for technical qualification is 80%. • Other applications for Benchmark – LAMMPS, P3DFFT, GROMACS. (https://www.tifrh.res.in/webdata/documents/events/tenders/2024/mar/bm2024.zip) Note: All the above benchmark codes including HPL should be run using open source gnu compilers 9.0 or above, openmpi 4.0 or above. Other commercial compilers or any code optimizations are not allowed for benchmarking. Provide read-only output of the above benchmark on DVD/CD along with the bid. Do not provide the print out of the outputs. Proposals of vendors who do not fulfil the above criteria or who fail to submit documentary proof would be rejected.	
	Clarification Sought: As the processor OEM has limited processors of this specification and limited nodes are available for benchmarking, it is requested to amend the Tera Flop ratings from 10 TF, 30 TF and 50 TF to 9 TF, 18 TF, 27 TF and 36 TF. Requesting to allow AMD's AOCC compiler also along with the open source compilers.	**TIFR Hyderabad Response:** The request will be considered for evaluation by the technical committee
3	**Page No – 9, Point no – 5** **BeeGFS based Parallel File System (PFS) with following specifications**	

		Clarification Sought: Do the bidders have to quote 5 year OEM supported BeeGFS software or quote for 1 year OEM support and provide further bidder support?	**TIFR Hyderabad Response:** Bidders are requested to implement the latest stable open source community edition of Bee GFS and the features available only on the community edition. Bidders are free to quote enterprise edition. During the warranty period Bee GFS should be fully supported by the successful bidder / OEM.
4	**Page No – 17** **Delivery time:** Delivery period will be 30 weeks from the date of purchase order. Once delivered to onsite, the installation, commissioning and acceptance testing period will be within 4 weeks from the date of delivery of equipment. Delay in delivery will have penalty. TIFR-H reserves the right to cancel the order if it is not deployed even after that.		
		Clarification Sought: As the computing interconnects OEM delivery lead time is 52 Weeks, it is requested to increase the delivery time to 52 weeks.	**TIFR Hyderabad Response:** The request will be considered for evaluation by the technical committee

प्रशासनिक अधिकारी / **ADMINISTRATIVE OFFICER**
(क्रय अनुभाग) / **(PURCHASE SECTION)**
टीआईएफआर हैदराबाद / **TIFR HYDERABAD**