


Libin T. T

C-DAC

THIRUVANANTHAPURAM



**RISC-V ARCHITECTURE
AND
DIR-V VEGA PROCESSOR &
ECOSYSTEM**

The talk will delve into the advancements and opportunities associated with cutting-edge RISC-V based architecture developed under India's Digital India initiative. It will highlight the growing popularity of the DIR-V VEGA Processor, fueled by government support, industry adoption, and educational integration.

Attendees will explore the processor's design and verification aspects, focusing on its performance, energy efficiency, and rigorous verification methods. Additionally, the session will cover the supporting software ecosystem, including development tools, operating systems, and middleware, as well as the role of developer communities and industry groups. Join us to gain a comprehensive understanding of how the DIR-V VEGA Processor is driving technological innovation in India and discover the tools and resources available for engaging with this transformative technology.

DEC**10th****2024**

TIFRH Auditorium 14:30 Hrs

Tea/Coffee 14:15 Hrs